

▲

B

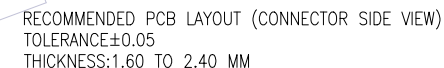
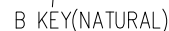
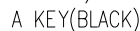
- C

D

E

F

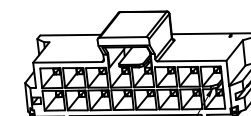
G

ABCDEEGH

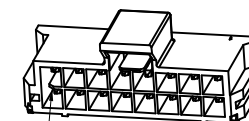
HSF

Table1

PIN Current AWG Rating	Ckt Size(Dual Rows)						
	2*2	2*3	2*4	2*5	2*6	2*7	2*8
20	8.0	7.7	7.5	7.2	7.0	6.7	6.5
22	6.5	6.1	5.8	5.5	5.1	4.8	4.5
24	6.0	5.7	5.5	5.2	5.0	4.7	4.5
26	5.0	4.7	4.5	4.2	4.0	3.7	3.5

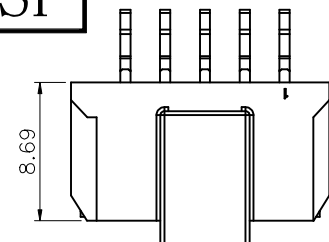


A KEY(BLACK)

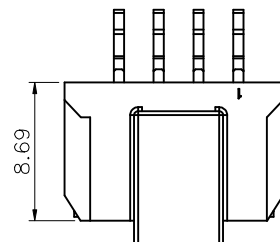


B KEY(NATURAL)

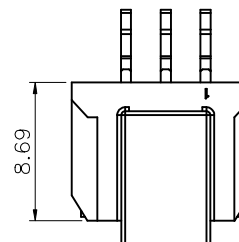
* Note: KEY structure diagrams



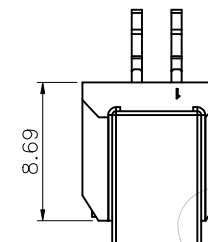
5 CKTS



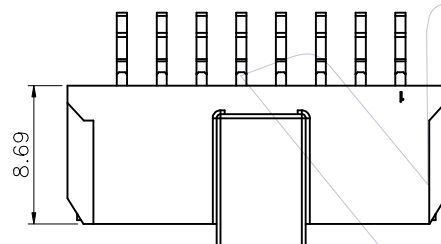
4 CKTS



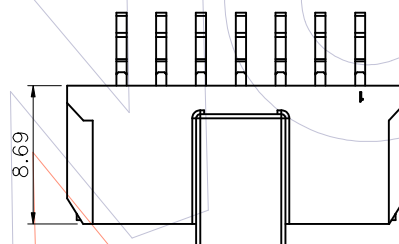
3 CKTS



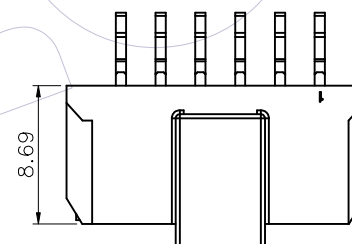
2 CKTS



8 CKTS



7 CKTS



6 CKTS

REV	DATE	MODIFICATION DESCRIPTION	CHANGE	OPERATION	DRAW	DATE	SCALE	1:1	PART NO.
A0	8/19'23	NEW	Dean.Liu	X.X ±0.40	Dean.Liu	8/19'23	UNIT	mm	WF2508-2WZHXXXXXX1
				X.XX ±0.25	Dean.Liu	8/19'23	SIZE	A4	TITLE: 2.5*2.5mm Wafer Dual Rows 90°SMT
				X.XXX ±0.15	Tom.dai	8/19'23	SHEET	2/2	Customer NO.
				Angle ± 3'			PROJ.		
				DIM TOL					

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